



PATENT APPLICATION
Docket No. 5484-093
Client No. PX1392-US/SSD

2818
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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Byeong-Hoon Lee and Seung-Keun Lee

Serial No. 09/997,080 Examiner: Phan, Trong Q

Confirmation No. 6095

Filed: November 28, 2001 Art Unit: 2818

For: NONVOLATILE SEMICONDUCTOR MEMORY DEVICE

Mail Stop NON FEE AMENDMENT
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Responsive to the Office Action dated February 19, 2004, enclosed is an amendment in the above-identified application.

The fee has been calculated as shown below.

CLAIMS AS AMENDED					
For:	Number After Amendment	Previous Number	Extra	Rate	Additional Fee
Total Claims	22	22	0	x \$18 =	\$0
Independent Claims	5	5	0	x \$86 =	\$0
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT					\$0


*greater of twenty (20) or number for which fee has been paid

**greater of three (3) or number for which fee has been paid

- ☒ One replacement sheet drawing (comprises figure 3)
- ☒ One annotated sheet showing changes (comprises figures 3)
- ☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703.

Respectfully submitted,

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I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450
Date: May 12, 2004


Li Mei Vermilya



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AMENDMENT

Responsive to the Office Action, dated February 19, 2004, please amend the application as follows.

Amendments to the Drawings begin on page 2 of this paper.

Amendments to the Claims are reflected in the listing of claims which begins on page 3 of this paper.

Remarks/Arguments begin on page 8 of this paper.

An **Appendix** containing a replacement sheet for FIG. 3 is attached following page 13 of this paper.

IN THE DRAWINGS

A replacement sheet for FIG. 3 is attached in the Appendix following page 12 of this paper. The replacement sheet for FIG. 3 plainly shows that the reference numeral "100" refers only to the sector structure 100, and not to the memory cell array 101 that is contained within the sector structure 100.